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CMOS VLSI Design: A Circuits and Systems Perspective, 4th Edition Neil Weste, Macquarie University and The University of Adelaide, Macquarie University and The University of Adelaide David Harris, Harvey Mudd College

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1: Circuits & Layout CMOS VLSI Design Slide 8 Moore's Law q1965: Gordon Moore plotted transistor on each chip - Fit straight line on semilog scale - Transistors 4004 8008 8080 8086 80286 Intel386 Intel486 Pentium Pro Pentium II Pentium II Pentium 4 1,000 10,000 100,000 1,000,000 ...

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